

a mean calculator for averaging the phase differences detected by the phase detector for [a] one symbol and generating the mean phase difference value; and
a normalizer for normalizing the mean value obtained by the mean calculator by dividing [it] the mean phase difference value into reference values corresponding to phase differences generated when an FFT window error of one sample exists.

7. (Amended) The OFDM receiver for interlocking FFT window position recovery with sampling clock control as claimed in claim 4, wherein the normalization of the normalizer is carried out by multiplying $\frac{N}{2\pi}$ [to] by the mean value.

REMARKS

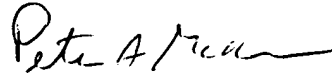
General remarks:

Claims 1-7 are all the claims pending in the application. The specification is amended to correct noted informalities. Claims 1, 3, 4, and 7 are amended for clarity and to correct informalities. Applicant respectfully requests the Examiner to enter this Amendment prior to examining the application.

PATENT APPLICATION
09/555,046 (Q58827)

Applicant hereby petitions for any extension of time which may be required to maintain the pendency of this case, and any required fee, except for the Issue Fee, is to be charged to Deposit Account No. 19-4880.

Respectfully submitted,



Peter A. McKenna
Registration No. 38,551

SUGHRUE, MION, ZINN,
MACPEAK & SEAS, PLLC
2100 Pennsylvania Avenue, N.W.
Washington, D.C. 20037
(202) 293-7060

Date: August 11, 2000

09/555,046-Q58827